



Brennan 2-81-1-4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): J.M. Brennan et al.
Case: 2-81-1-4
Serial No.: 10/773,614
Filing Date: February 6, 2004
Group: To Be Assigned
Examiner: To Be Assigned

Title: Semiconductor Device Configured for
Reducing Post-Fabrication Damage

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature:  Date: March 29, 2004

TRANSMITTAL OF FORMAL DRAWINGS

Mail Stop PGPUB Drawings
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attention: Official Draftsperson

Sir:

Applicants submit herewith four (4) sheets of formal drawings in the above-referenced application.

Respectfully submitted,



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Date: March 29, 2004